

A¹
Coupled Ceramic Capacitors & Method For The Manufacture Thereof--

Please replace the paragraph beginning at line 22 of page 3, with the following rewritten paragraph:

A²
5 --the capacitors are disposed at substantially plane-symmetrical positions on two opposite surfaces of the circuit board and substantially identical voltages are applied to the capacitors,
wherein the electronic circuit is of a type in which voltages applied to the capacitors have frequencies varying in an audible frequency band.--

10 Please delete the paragraphs beginning at line 26 of page 3 and ending at line 19 of page 4.

IN THE CLAIMS

22 Please delete the pending claims and insert therefore the following claims:

Rule 26
Sub B7
A³
18 21 (New) A method for mounting multilayered ceramic capacitors on a circuit board having a front surface and a back surface, wherein each capacitor includes a body having dielectric layers formed of a dielectric ceramic material and internal electrode layers and a pair of external terminal electrodes formed on two sides of the body, the dielectric layers and the internal electrode layers being stacked alternately in the body and the internal electrode layers being connected in parallel to the external terminal electrodes in an alternate manner, the method comprising the steps of:

forming lands at substantially plane-symmetrical positions on the front and the back surfaces, wherein every two lands disposed at their substantially plane-symmetrical positions are connected each other; and

25 mounting the capacitors on the lands of the front and the back surfaces and electrically coupling the external terminal electrodes of the capacitors to the lands on the front and the back surfaces.